

FIG. 1

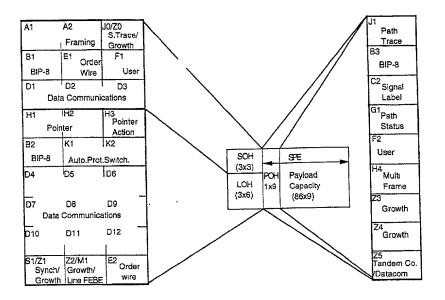


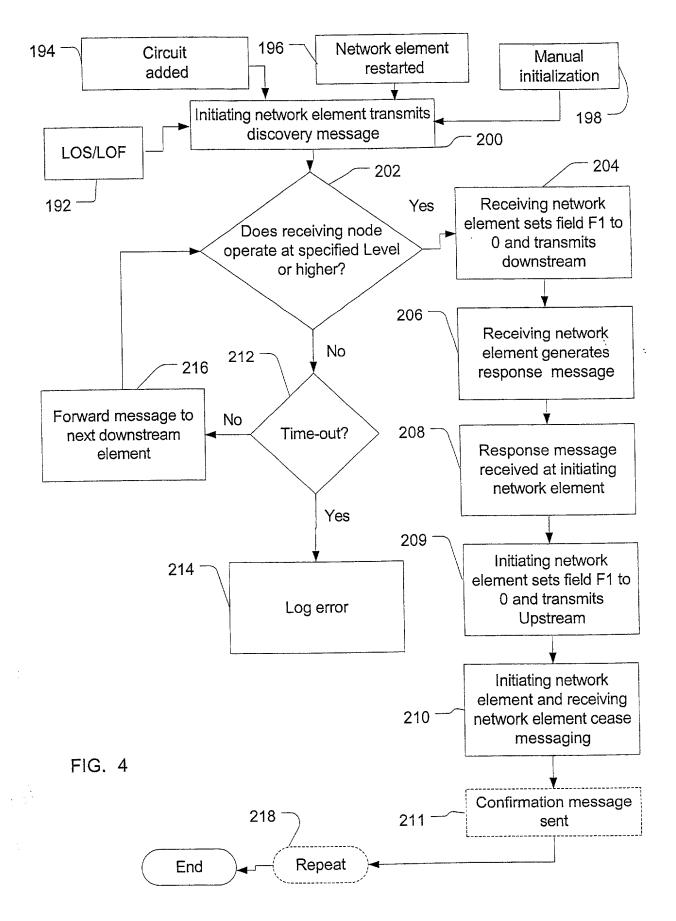
FIG. 2 Conventional Art

: :

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9 10 11 12 13 14 15	4	X X P1 P2 P3 F4 F5		Port identifier	F	14
7 8		×		Node Identifier		
9		×				F3
S		×				
4		×				
3		×				
2		ĭ		DisId		F2
1		H		CRC Level DisId		F1
0		FS		CRC		F0
Byte		Iohol	Lauci	Description		Field

FIG. 3



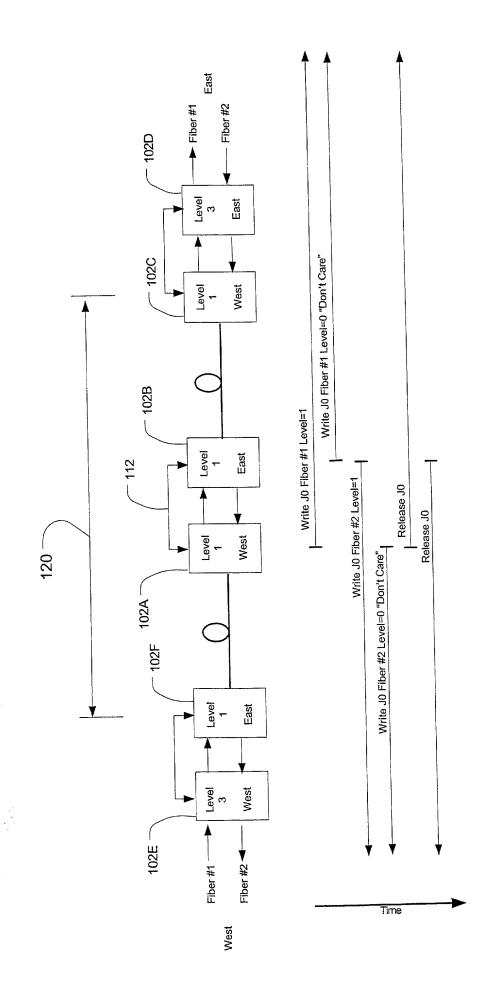


FIG. 5A

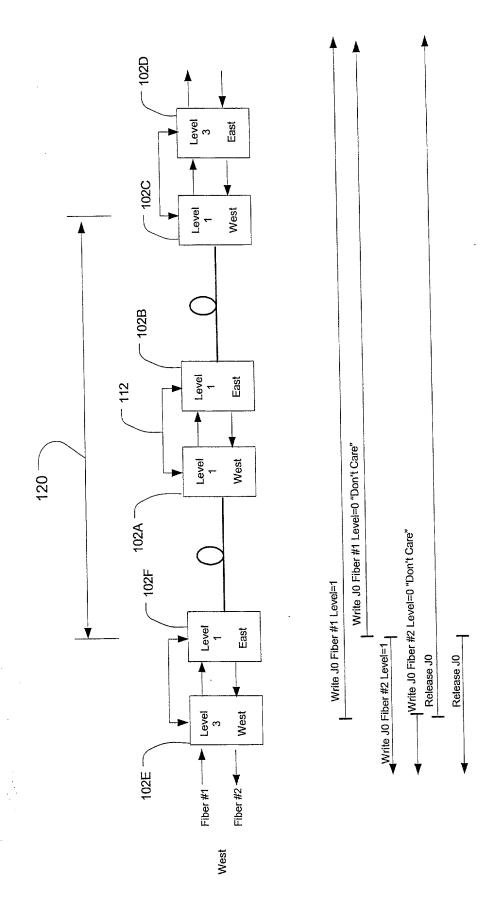


FIG. 5B

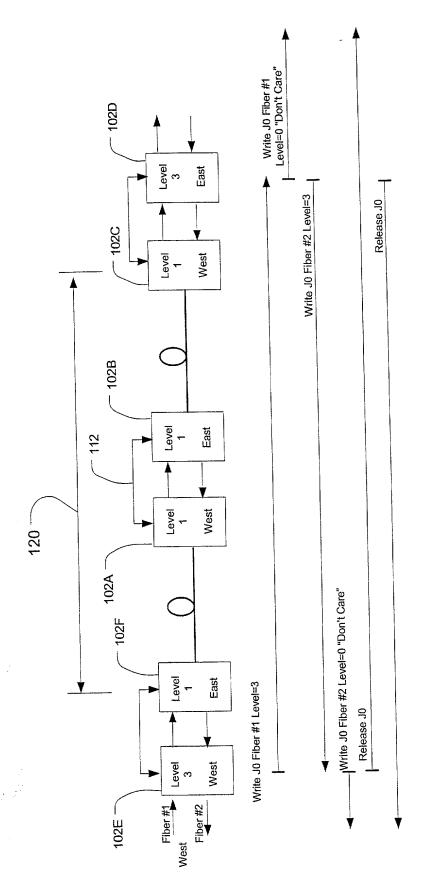
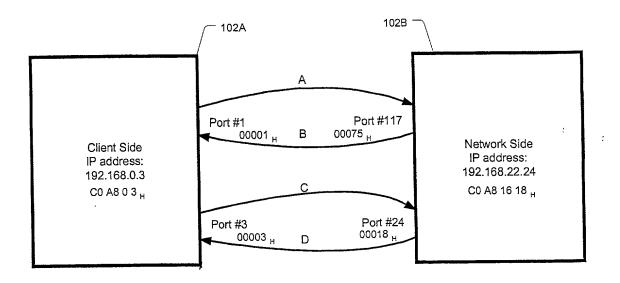
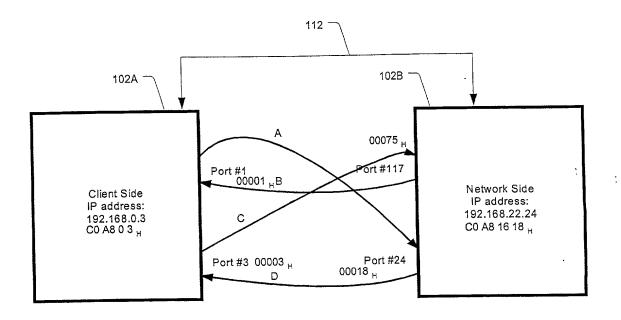


FIG. 5C



- A 1 P C0A80003 00001
- B 1 P C0A81618 00075
- C 1 P C0A80003 00003
- D 1 P C0A81618 00018

FIG. 6



A 1 P C0A80003 00001

B 1 P C0A81618 00075

C 1 P C0A80003 00003

D 1 P C0A81618 00075

FIG. 7

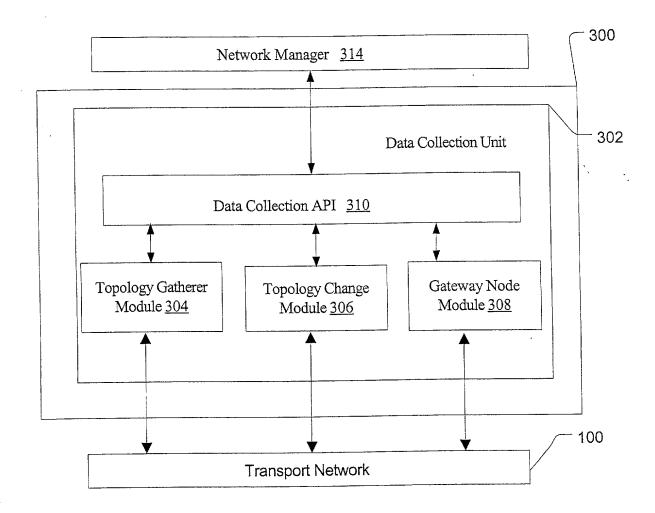


FIG. 8

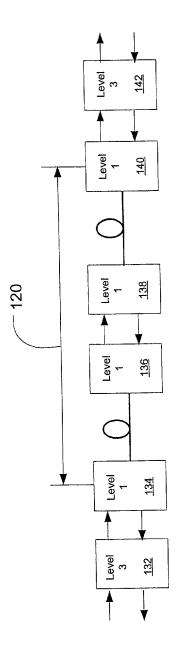
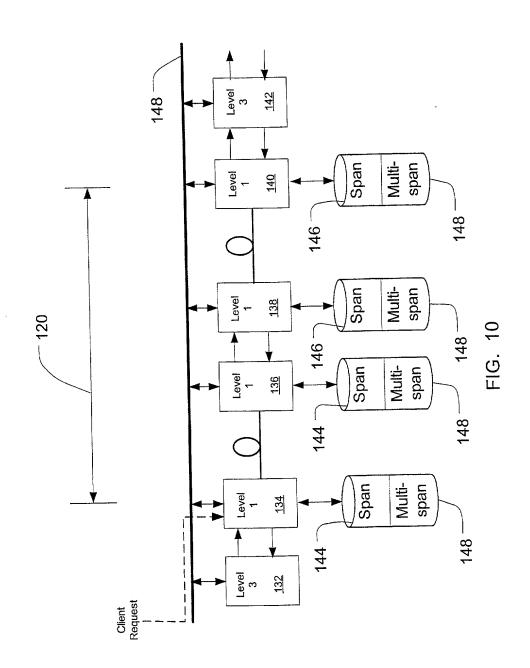
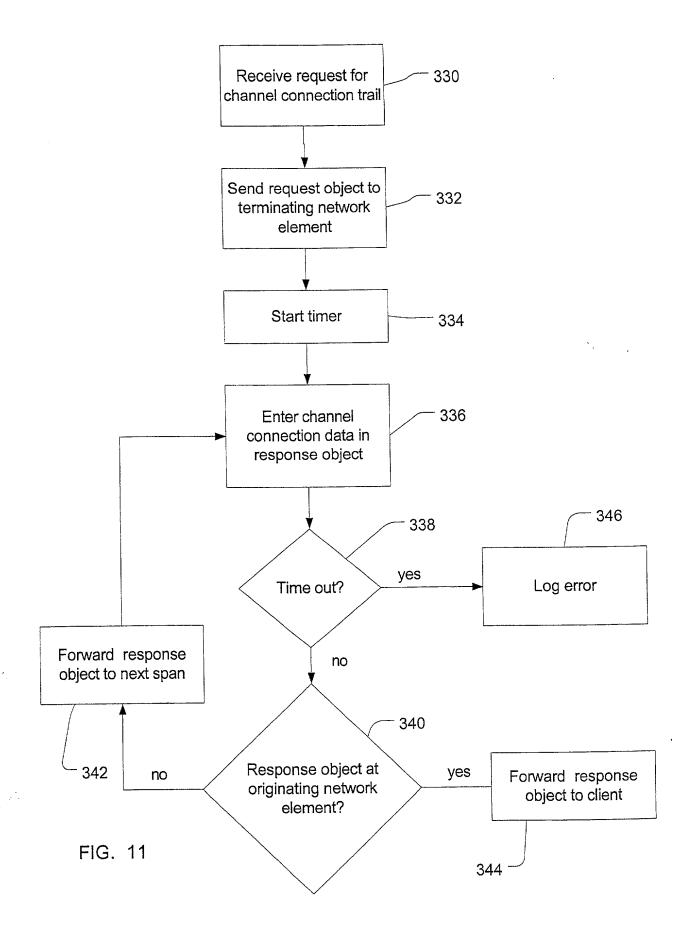


FIG. 9





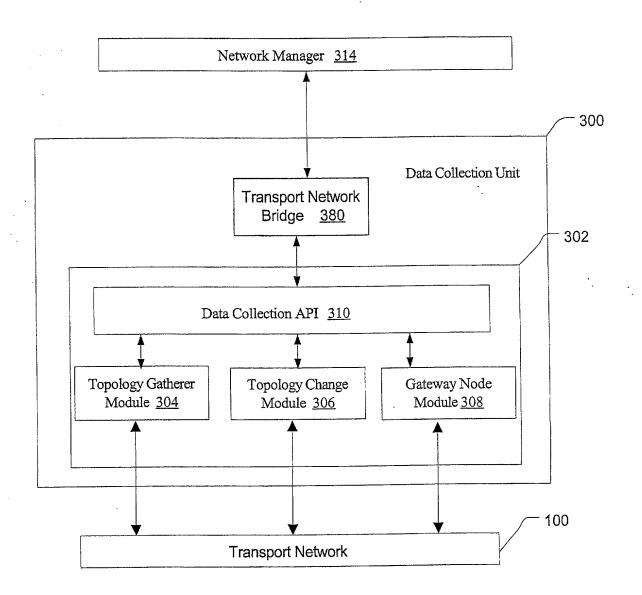


FIG. 12

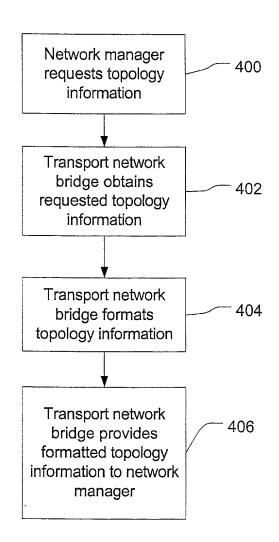


FIG. 13

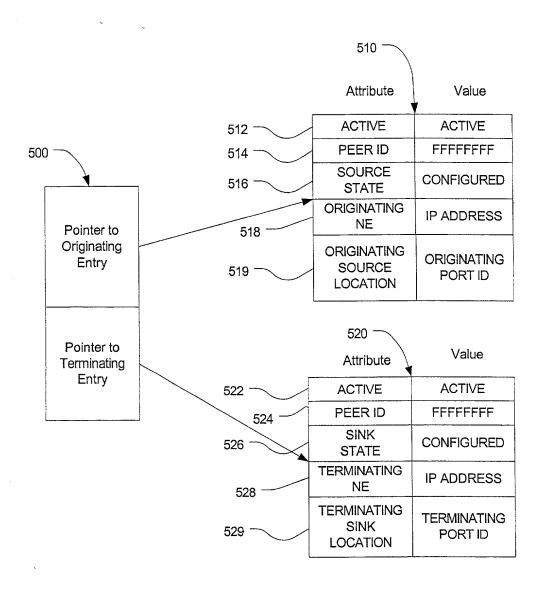


FIG. 14

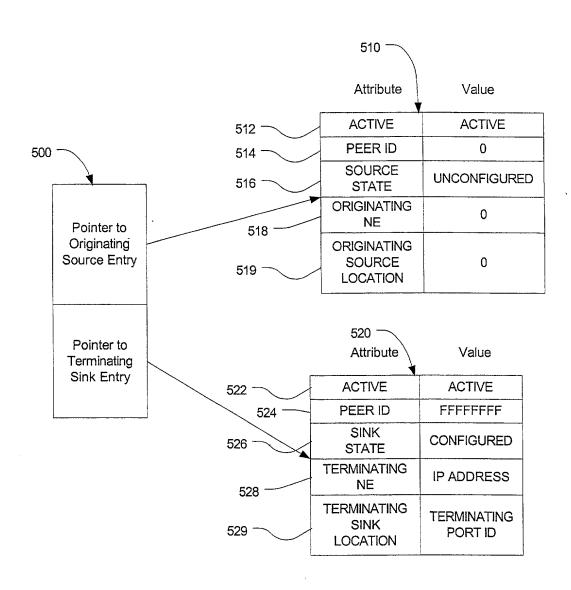


FIG. 15

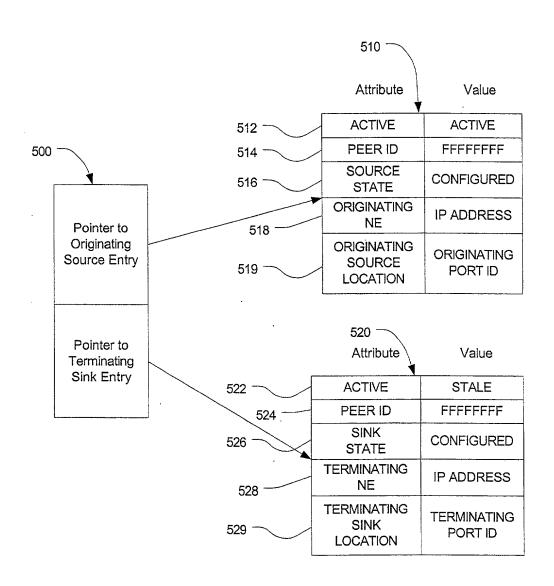


FIG. 16

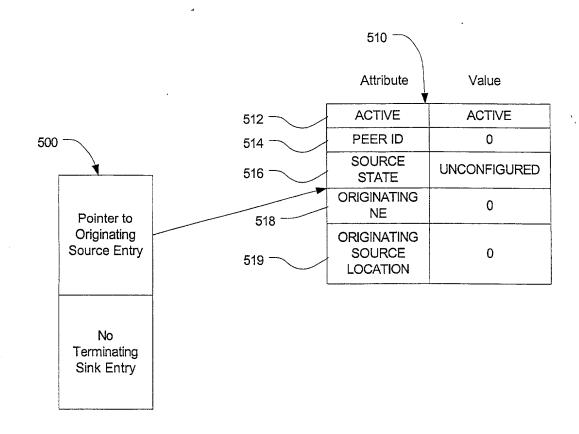


FIG. 17